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Ahmet Lale, Auriane Grappin, David Bourrier, Laurent Mazenq, Aurélie Lecestre, et al.. Development of all-around, SiO<sub>2</sub>/Al<sub>2</sub>O<sub>3</sub> gate, suspended silicon nanowire chemical field effect transistors Si-nw-ChemFET. EUROSENSORS XXXI, Sep 2017, Paris, France. hal-01687839

**HAL Id: hal-01687839**

**<https://laas.hal.science/hal-01687839>**

Submitted on 7 Feb 2018

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# Development of All-around SiO<sub>2</sub>/Al<sub>2</sub>O<sub>3</sub> Gate, Suspended Silicon Nanowire Chemical Field Effect Transistors Si-nw-ChemFET

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Presented at the **EuroSensors 2017 Conference**, Paris, France, September 3–6, 2017.

Published: 8 August 2017

## Abstract

We present a sensor platform associated to silicon-nanowire chemical field effect transistors (Si-nw-ChemFET). Innovations concern the use of networks of suspended silicon N<sup>+</sup>/P/N<sup>+</sup> nanowires as conducting channel, the realization by thermal oxidation and Atomic-Layer Deposition (ALD) of a SiO<sub>2</sub>/Al<sub>2</sub>O<sub>3</sub> gate insulator all-around the silicon nanowires, and their final integration into covered SU8-based microfluidic channels. The Si-nw-MOSFET/ChemFET fabrication process and electrical/electrochemical characterizations are presented. The fabrication process did not need an expensive and time-consuming e-beam lithography, but only fast and “low cost” standard photolithography protocols. Such microdevice will provide new opportunities for bio-chemical analysis at the micro/nanoscale.

## Keywords

ChemFET, finFET, MOSFET, ISFET, silicon nanowire, biosensor, potentiometric sensor, nanosensor, microsensor, pH measurement, microfluidics, gate all-around.

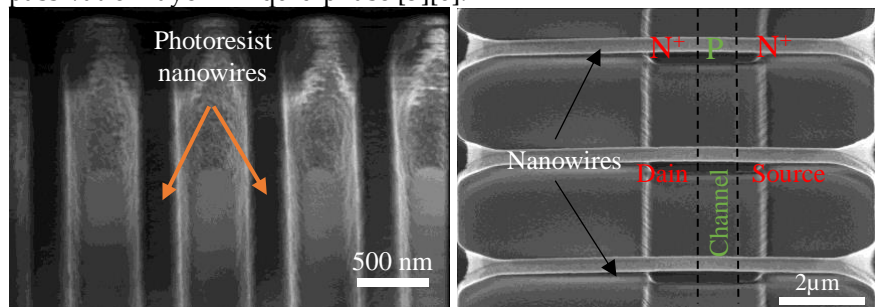
## 1. Introduction

Chemically sensitive Field Effect Transistors (ChemFET) are microsensors derived by Metal Oxide Semiconductor Field Effect Transistors [1]. These are electronic microsensors designed to measure the pH. ChemFETs have shown interesting properties for the detection in liquid phase [1][2] and have been functionalized for many applications. Nevertheless, to open the door to new innovative applications for ChemFET-based microsensors, it is necessary to increase their sensitivity, to decrease response times and their detection limit. To meet this challenge, the development of nanotransistor based on silicon nanowire Si-nw-ChemFET was proposed [3][4]. In this field, surrounding gate seems the ideal candidate: indeed, low channel dimensions provide new detection mechanisms as well as analysis of micro/nano-volumes, allowing to work at the single cell level [4]. Thus, the development of suspended silicon nanowires should still improve Si-nw-ChemFET detection properties in liquid phase, providing the integration of well-controlled chemically sensitive gate insulators all-around the Si-nw-based conductive channel.

## 2. Device fabrication

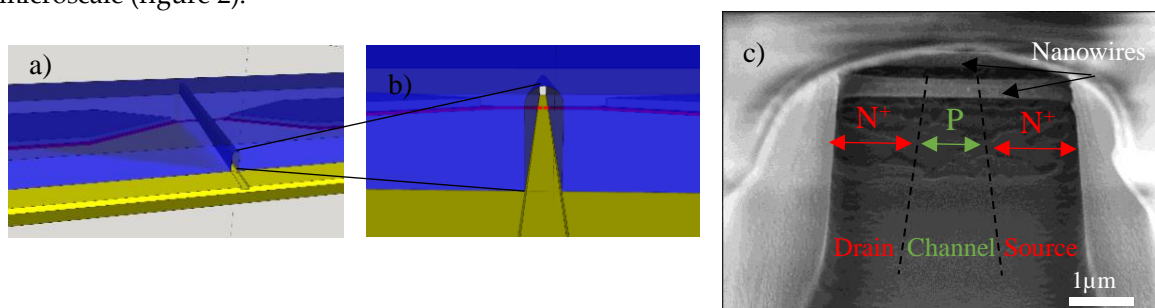
6-inch SOI wafers (Silicon On Insulator, P type, 10<sup>15</sup> atoms/cm<sup>3</sup>) were used. These devices consist in single and networked silicon nanowires field effect transistors. Different wire lengths, gate lengths and number of parallel wires were fabricated. First, source and drain areas were N doped (10<sup>20</sup> atoms/cm<sup>3</sup>) with arsenic ion implantation. Two different gate length were realized, 0.9 μm and 3.9 μm. To fabricate silicon nanowires, projection photolithography with a Canon FPA-3000i4 stepper and an AZ ECI 3012 photoresist were employed. After optimizations, were achieved on a single chip, patterns of transistors with a single nanowire as channel, next to patterns of transistors with 100 nanowires parallel network (figure 1). The goal is to compare in the future, the pros and cons of each

structure, in terms of limit of detection and/or sensitivity for biochemical applications. Once photoresist nanowires were optimized, nanopatterns were transferred in silicon thanks to a reactive ion etching. After optimization,  $200\text{nm} \times 170\text{nm}$  rectangle section nanowires were obtained with a perfect reproducibility. Two different densities of network were realized: 20 and 100 nanowires over a width of  $80\mu\text{m}$ . Thus, using SOI wafers, networks of suspended silicon nanowires were successfully integrated. Then, the realization of the gate structure was performed. After RCA cleaning, a dry thermal oxidation of  $22\text{nm}$  was achieved and an alumina film of  $26\text{nm}$  was deposited by ALD. The advantage of this technique is to deposit extremely conformal films all around the nanowires, while providing well-controlled thicknesses [5]. Thus, a  $\text{SiO}_2/\text{Al}_2\text{O}_3$  gate was integrated all-around the silicon nanowire. This  $\text{Al}_2\text{O}_3$  layer was used as a gate chemically sensitive layer as well as the microdevice passivation layer in liquid phase [3][6].



**Figure 1.** SEM views, a) of a network of photoresist nanowires, before reactive ion etching ; b) of silicon nanowires after gate insulator completion (tilt  $52^\circ$ ).

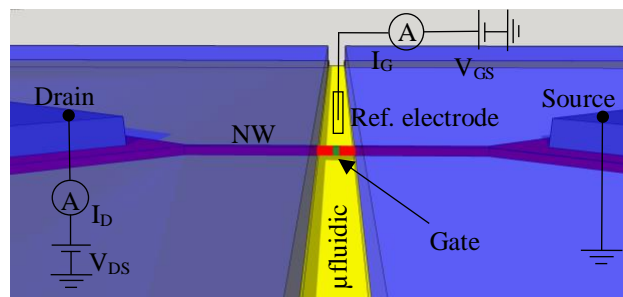
Once the gate/source/drain metallic contacts and interconnections were ended, Si-nw-MOSFET and Si-nw-ChemFET were fabricated. Then, using a specific SU8-3D technique [7], covered  $5\mu\text{m}$  width microfluidic channels were finally realized in order to deal with liquid phase analysis at the microscale (figure 2).



**Figure 2.** a) and b) schematic views of the Si-nw-ChemFET and the microfluidic channel. c) SEM view of suspended silicon nanowires inside a covered microfluidic channel (tilt  $52^\circ$ ).

### 3. Device characterizations

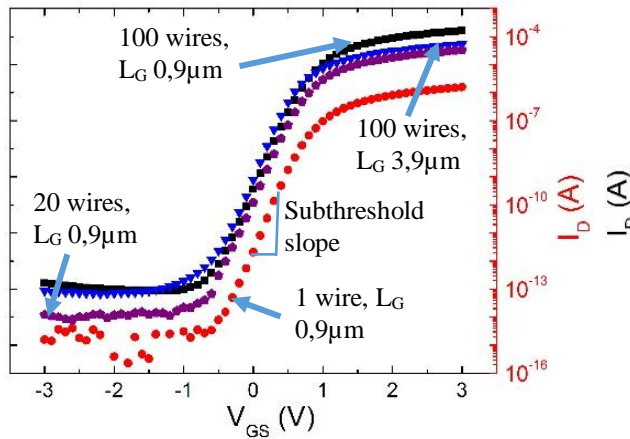
Si-nw-MOSFET/ChemFET were characterized electrically and electrochemically using standard I-V experiments (figure 3). The MOSFET works without liquid, so it makes easier some characterizations. It allows to check the quality of the manufacturing process, and to discriminate problems due to the transistor from those related to the microfluidics.



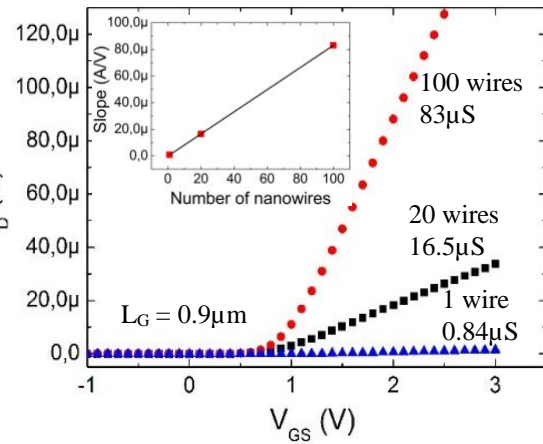
**Figure 3.** Schematic view of the polarization for Si-nw-ChemFET characterizations.

### 3.1. Si-nw-MOSFET characterizations

The threshold voltage of 42 transistors was measured and the mean value was 0.62V with a standard deviation of only 0.26V. That shows the quality of the SiO<sub>2</sub>/ALD-Al<sub>2</sub>O<sub>3</sub> gate dielectric and the Si/SiO<sub>2</sub> interface. The influence of 2 parameters on electric characteristics was studied: the number of parallel nanowires (1, 20 and 100) and the gate length  $L_G$  (0.9 $\mu$ m “short” and 3.9 $\mu$ m “long”), the length of the nanowires is 10 $\mu$ m. Firstly, very low subthreshold ( $I_{OFF}$ ) currents (7.10<sup>-14</sup>A for  $V_{GS} = -2$ V,  $V_{DS} = 0.1$ V) were obtained for a parallel network of 100 nanowires (figure 4). This leakage current is the same for both gate lengths and proportional to the number of parallel nanowires. Therefore, it allows to estimate the leakage current of X nanowires. The leakage current through the gate dielectric ( $I_G$ ) is lower than 10<sup>-15</sup>A (limits of the measuring instrument) for 100 parallel nanowires under 3V. So, the  $I_G$  current is lower than 10<sup>-17</sup>A for one nanowire and shows the insulation quality of the SiO<sub>2</sub>/Al<sub>2</sub>O<sub>3</sub> double layer. The number of parallel nanowires and the gate length does not influence the subthreshold slope, which is 10<sup>6</sup>/V. The ratio  $I_{ON}/I_{OFF}$  is about 10<sup>9</sup>, thanks to the very low  $I_{OFF}$  current. The transconductance is 4.3 times higher for nanowires with the short gate length in comparison with the long gate length, exactly the length ratio between the long and short gate lengths. The transconductance is also proportional to the number of parallel nanowires (figure 5). All these characterizations have confirmed the good operation of Si-nw-MOSFET.



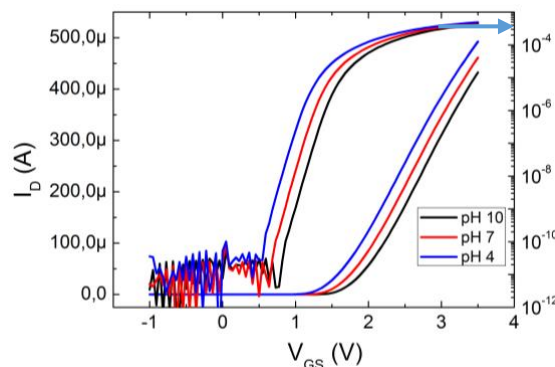
**Figure 4.**  $I_d(V_{gs})$  characterizations of Si-nw-MOSFETs, semi-logarithmic scale. ( $V_{ds} = 0.1$ V)



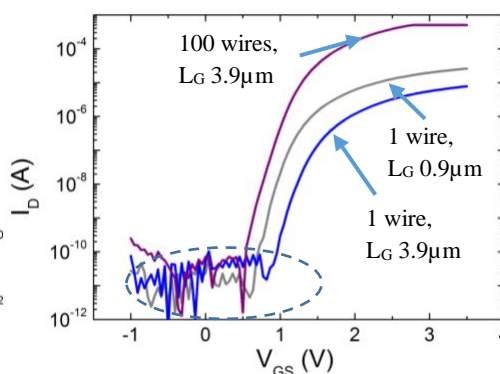
**Figure 5.**  $I_d(V_{gs})$  characterizations of Si-nw-MOSFETs, linear scale. ( $V_{ds} = 0.1$ V)

### 3.2. Si-nw-ChemFET characterizations

The Si-nw-ChemFET is like a Si-nw-MOSFET which gate metallization is replaced by a microfluidic channel. Si-nw-ChemFETs were characterized in liquid phase with a specific setup. The liquid was polarized with an Ag/AgCl/KCl sat reference electrode. Like for the study of Si-nw-MOSFET, the influence of 2 parameters on electric characteristics was studied: the number of parallel nanowires (1 and 100) and the gate length  $L_G$  (0.9 $\mu$ m and 3.9 $\mu$ m), the length of the nanowires is 10 $\mu$ m. The subthreshold leakage current ( $I_{OFF}$ ) and the leakage current through the gate dielectric ( $I_G$ ), for 100 parallel nanowires, were not measurable because they were lower than the limit of the characterization setup (5.10<sup>-11</sup>A) (figure 7). That means an  $I_{OFF}$  current and an  $I_G$  current lower than 5.10<sup>-13</sup>A for one nanowire. These very low leakage currents allow a high  $I_{ON}/I_{OFF}$  ratio reaching at least 10<sup>7</sup> for one nanowire and 10<sup>8</sup> for 100 parallel nanowires. Concerning the subthreshold slope, excellent characteristics were obtained, with 10<sup>8</sup>/V. Like for the Si-nw-MOSFET, this value is independent of the number of parallel nanowires and gate length. As for the Si-nw-ChemFET, the transconductance is inversely proportional to the gate length. The Nernstian response to pH variations of 58mV/pH is in agreement with theory (figure 6). All these characterizations have shown the good behavior of Si-nw-ChemFET in liquid phase, and demonstrated the performances of the bilayer SiO<sub>2</sub>/ALD-Al<sub>2</sub>O<sub>3</sub> for ChemFET fabrication.



**Figure 6.**  $I_d(V_{gs})$  characterizations in liquid phase of Si-nw-ChemFET, with a semi-logarithmic and a linear scale, for three different pH. ( $V_{ds} = 1V$ )



**Figure 7.**  $I_d(V_{gs})$  characterizations in liquid phase of Si-nw-ChemFETs, with a semi-logarithmic scale. ( $V_{ds} = 1V$ )

#### 4. Discussion

These good results are due to the excellent control of the nw-Si/insulator/electrolyte interface using an all-around gate with high dielectric capacity. Junctions on the nanowires make possible to increase the  $I_{ON}/I_{OFF}$  ratio by decreasing strongly the current when the transistors are in their blocked mode. The reduction of the gate length make possible to increase the transconductance, it is interesting for the amplification of biochemical signals like action potentials [8]. The bilayer  $SiO_2/Al_2O_3$  is advantageous because the Si/ $SiO_2$  allows a very good interface, which is highly important for a MOSFET based component. The second layer, the ALD- $Al_2O_3$  is a good (high-k) insulator in liquid phase. That is why gate leakage currents are so low. Thanks to  $Al_2O_3$ , Si-nw-ChemFET shows near Nernstian responses to pH changes. This allows to hope for a much better signal-to-noise ratio for biological applications than planar ChemFET.

#### 5. Conclusion

Silicon nanowire based MOSFET and ChemFET (Si-nw-MOSFET and Si-nw-ChemFET) were designed, realized and presented in this paper. Electrical and electrochemical characterizations have shown good results. In short-term perspective, thanks to the small dimensions of the sensing area and to improved detection performances, the accurate and reproducible monitoring of biological metabolisms is expected on living cells using suspended Si-nw-ChemFET.

#### Acknowledgements

This work was partly supported by LAAS-CNRS micro and nanotechnologies platform, member of the French RENATECH network.

**Conflicts of Interest:** the authors declare no conflict of interest.

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