

3D RuO₂ microsupercapacitor electrodes based on Si nanowires, nanotrunks and nanotrees

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Energy autonomy keeps being a critical issue in the context of off-grid applications, such as autonomous wireless sensor networks, mostly being served by primary batteries. One solution to obtain a complete energetic autonomy of embedded microsystems, is to harvest and store the energy available in the environment, such as heat or vibrations. 3D on-chip microsupercapacitors show very interesting characteristics when it comes to these applications, thanks to their long lifetime and high power density [1].

In this work, pseudocapacitance of hydrous ruthenium oxide coated onto nanostructured silicon electrodes was investigated. Electrodeposition of hydrous ruthenium oxide was performed at room temperature by cyclic voltammetry from a 5 mM ruthenium chloride solution RuCl₃ on different nanostructured silicon electrodes (nanowires, nanotrunks and nanotrees) obtained by PECVD [2].

The resulted ruthenium oxide deposition was characterized by cyclic voltammetry in 0.5 M H₂SO₄ at different scan rates and by scanning electron microscopy (SEM). Capacitive behaviors were obtained for scan rates greater than 100 mV s⁻¹ with a specific capacitance as high as 18.8 mF cm⁻² for the coated silicon nanowires electrodes at 200 mV s⁻¹. This type of electrode material design is very promising for the integration of microsupercapacitors in wearable electronic devices and self-powered micro-systems, in which the key consideration is to minimize footprint occupancy while maximizing the energy.

[1] N.A. Kyeremateng, T. Brousse, D. Pech, microsupercapacitors as miniaturized energy-storage components for on-chip electronics, *Nat. Nanotech.*, vol. 12, p. 7 (2017).

[2] D. Gaboriau, D. Aradilla, M. Brachet, J. Le Bideau, T. Brousse, G. Bidan, P. Gentile, S. Sadki, Silicon nanowires and nanotrees: elaboration and optimization of new 3D architectures for high performance on-chip supercapacitors, *RSC Adv.*, vol. 6, p. 81017 (2016).